



YAG Resistor Trim Tips & “Rules Of Thumb“

Questech trim standard is MIL-STD-883, notice 12, method 2032 unless otherwise specified on customer drawing, purchase order or other agreement with Questech.

Plate size limitation for trim: 5 inch x 5 inch.

Nominal trim kerf widths:

Thick film: .0035 inch
Thin film: .0012 inch

Achievable* tolerances:

<u>Resistor Nominal In Ohms</u>	<u>Tolerance Capability*</u>
25<nom<50	+/- 0.25 ohms
25<nom<50	+/- 1%
50<nom<10K	+/-0.5%
10K<nom<100K	+/-1%
100K<nom<250K	+/-2%
250K<nom<1meg	+/- 5%
1 meg<nom	Not performed

*Depends on print design, stability, geometry and pre trim value with respect to nominal.

Trim type will generally affect overall accuracy and cost:

<u>Trim Type</u>	<u>Accuracy/Stability</u>	<u>Cost</u>
Plunge	Least	Least
L	Good	Medium
Scrub**	Best	Highest

**May be desirable for symmetry in RF applications or for power dissipation considerations.

“Active guard” trim of resistor loops is not particularly reliable and should thus be avoided.

All trims are orthogonal to X-Y axis. Call for exceptions.

Questech does not perform voltage, capacitance or active trims.

Standard reject marking is either water soluble or permanent ink as customer specifies. Rejects can be laser marked upon special request.

Trim of thick film resistors printed on dielectric (multilayer) is routinely performed.

Substrates acceptable for trim: Alumina aluminum nitride BEO

Questech has capability for trim of thermistors.

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